

RuQing G. Xu

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Education

The University of Tokyo <i>Ph.D. in Physics expected in 2024</i>	Oct. 2021 – Sept. 2024 Tokyo, JP
The University of Tokyo <i>M.S. in Physics</i>	Sept. 2019 – Sept. 2021 Tokyo, JP
University of Science and Technology of China <i>B.S. in Physics</i>	Sept. 2015 – Jul. 2019 Hefei, CN

Industrial Experience

Deep Learning Algorithm Engineering Intern (Team undisclosed) <i>NVIDIA</i> <ul style="list-style-type: none">Performance optimizations for various deep learning tasks.	Jan. 2023 – Present Tokyo, JP
Deep Learning Algorithm Engineering Intern (cuTENSOR) <i>NVIDIA</i> <ul style="list-style-type: none">Special techniques for medium-size performance improvements.Tackled multiple (unusual) underperforming cases.In-depth L2 bandwidth analysis & optimizations.	Sep. 2021 – Jan. 2022 Tokyo, JP

Selected Research Projects

Speeding up Markov Chain Samplings on Fermionic Pair Products <i>Optimizations for the many-variable Variational Monte Carlo</i> <ul style="list-style-type: none">6× speed up for sampling phases via blocked updating techniques.3× speed up for estimation phase via query batching.	Jul. 2020 - Present Univ. Tokyo & Waseda Univ.
GEMMFIP <i>Unifying GEMM implementations in BLIS</i> <ul style="list-style-type: none">Fuse packing with the first computational pass over each input tile.A unified technique for implementing matrix operations that can achieve high performance across the problem size spectrum.Prototype implementation (available to all on GitHub) outperforms OpenBLAS, AOCL, and ArmPL.WIP: Automatic kernel generation via ExoLang.	Nov. 2022 - Present Collab. w/ the University of Texas at Austin
Arm SVE Kernels for BLIS <i>Tuned for Fujitsu A64FX on Supercomputer Fugaku</i> Collab. w/ Forschungszentrum Jülich & The University of Texas at Austin <ul style="list-style-type: none">BLIS outperforms vendor libraries on Supercomputer Fugaku (as of Jan. 2023).Talks presented in the Arm HPC User Group Workshop.Same set of kernels available also in TBLIS, provisional BLIS extension for direct tensor contraction.	Jul. 2020 - Sept. 2021
Customizing BLAS on Apple's Matrix Coprocessor <i>Personal work</i> <ul style="list-style-type: none">Crafted AMX2 machine code for BLIS & TBLIS.BLIS' peak performance is above Accelerate framework.	Jun. 2021 – Aug. 2022

Publications

<i>ArXiv:2302.08417</i> (ISC2023 submitted), RuQing G. Xu , Field G. Van Zee, Robert A. van de Geijn, <i>GEMMFIP: Unifying GEMM in BLIS</i>
<i>Comput. Phys. Commun.</i> 277 , 108375, RuQing G. Xu , Tsuyoshi Okubo, Synge Todo, Masatoshi Imada, <i>Optimized Implementation for Calculation and Fast-Update of Pfaffians Installed to the Open-Source Fermionic Variational Solver mVMC</i>
<i>Phys. Rev. Research</i> 3 , 023048, Xinliang Lyu, RuQing G. Xu , Naoki Kawashima, <i>Scaling dimensions from linearized tensor renormalization group transformations</i>
<i>J. Chem. Theory Comput.</i> 2019, 15 , 3, 1728-1742, James S. Spencer, Nick S. Blunt, ..., William A. Vigor, RuQing Xu , Alex J. W. Thom, <i>The HANDE-QMC project: open-source stochastic quantum chemistry from the ground state up</i>

Skills & Hobbies

Programming Languages: C, C++, CUDA, x86 Assembly, Arm64 Assembly, NVPTX, Julia, Python
Natural Languages: Chinese (native), English (GRE: 330; TOEFL: 108), Japanese (JLPT N1)